

REMARKS

Claims 14-41 are now pending in the application. The Examiner is respectfully requested to reconsider and withdraw the rejections in view of the amendments and remarks contained herein.

REJECTION UNDER 35 U.S.C. § 103

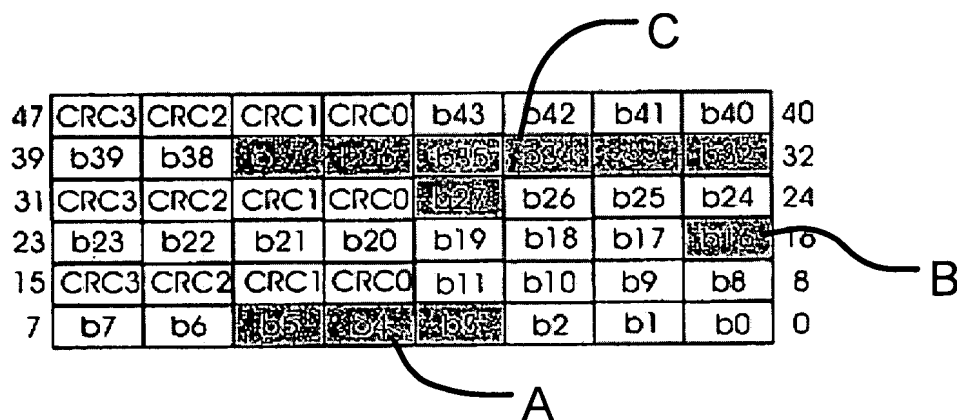
Claims 14-16, 18, 19, 24-26, 28, 29, 33-35, 37 and 38 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Aruga (U.S. Pub. No. 2003/0097525) in view of Budd et al. (U.S. Pat. No. 7,003,702).

With respect to claim 14, Aruga, either singly or in combination with Budd, fails to show, teach, or suggest a queue module that stores data lengths and data start addresses of first and second data segments and a read assembly module that reads a plurality of data blocks and extracts data segments from the read plurality of data blocks based on the data lengths and data start addresses **after the plurality of data blocks is read from the first memory.**

It is a longstanding rule that to establish a prima facie case of obviousness of a claimed invention, all of the claim limitations must be taught or suggested by the prior art. *In re Royka*, 180 USPQ 143 (CCPA 1974), see MPEP §2143.03. Furthermore, when evaluating claims for obviousness under 35 U.S.C. §103, all of the limitations must be considered and given weight. *Ex parte Grasselli*, 231 USPQ 393 (Bd. App. 1983), MPEP § 2144.03. Here, the alleged combination fails to disclose the limitation of extracting data segments from the read plurality of data blocks based on the data

lengths and data start addresses after the plurality of data blocks is read from the first memory.

As shown in an exemplary embodiment in FIG. 2 of the present application, a buffer 111 stores data segments A, B, and C as shown below:



The read assembly module first reads a plurality of data blocks that include, for example, data segment A. The read assembly module reads the plurality of blocks beginning at data block 0 and ending at data block 15 (i.e. 16 blocks of data) from the buffer 111. In other words, the read assembly module initially reads the entire plurality of data blocks, including desired (e.g. segment A) and undesired (e.g. CRC data blocks) data segments. After reading the plurality of data blocks, the read assembly module extracts data segment A from the plurality of data blocks based on a data start address and a data length stored in a queue module.

For example, please see Paragraph [0045], which states “[r]ead assembly module 403 sends to FIFO 404 the selected data segment, but the entire block where the segment resides is read so that CRC can confirm data integrity.” Here again, Applicants note that the read assembly module **first reads the entire 16 blocks of data from the memory, including the CRC data in blocks 12-15, and then**

subsequently extracts the data segment A based on the stored start address and the data length. Similarly, the read assembly module reads the 16 blocks of data beginning at data block 16 and ending at data block 31 and extracts the data segment B based on the stored start address and the data length.

The alleged combination of Aruga and Budd appears to be absent of any teaching or suggestion of this structure. The Examiner acknowledges that Aruga fails to disclose this limitation and instead relies on Budd. Applicants respectfully submit that Budd also appears to be absent of any teaching or suggestion of this limitation. For example, the Examiner notes that Column 23, Lines 23-37 of Budd disclose a queue module that stores data lengths and data start addresses of first and second data segments. The cited portion of Budd states:

Referring now to FIG. 20, shown is an example of an embodiment 900 of a scatter-gather list. A scatter-gather list may be implemented as an array as shown here, a linked list or other equivalent data structures as known to those skilled in the art. In this particular embodiment, an array may include one or more entries in which each entry includes information referencing a particular portion of memory. In particular, an entry includes a PTR field 902a a size field 902b and other information may also be included. The PTR field 902a may be a pointer or an address to a particular buffer area of memory. Size field 902b indicates the size of the buffer portion identified by the address in the field 902a PTR. Using this particular data structure, the actual physical location of data or memory locations associated with a logical data buffer may be represented. (Emphasis added)

The cited portion does not disclose reading a plurality of data blocks and extracting data segments from the plurality of data blocks based on the stored data lengths and data start addresses after reading the plurality of data blocks from the memory. In contrast, Budd appears to disclose, at best, **reading data based on the scatter-gather list**, but fails to disclose first reading a larger plurality of data blocks

from the memory and extracting only desired data segments from the plurality of data blocks **after the step of reading**. Applicants respectfully submit that reading data from a memory based on a scatter-gather list is not analogous to first reading a plurality of data blocks from memory and then extracting certain data from the data blocks after they are read from the memory.


Applicants respectfully submit that claim 14, as well as its dependent claims, should be allowable for at least the above reasons. Claims 24 and 33, as well as their corresponding dependent claims, should be allowable for at least similar reasons.

CONCLUSION

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action and the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested. If the Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at (248) 641-1600.

Respectfully submitted,

Dated: May 21, 2007

By: 
Damian M. Aquino
Reg. No. 54,964

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. Box 828
Bloomfield Hills, Michigan 48303
(248) 641-1600

MDW/DMA/lah